

PATENT APPLICATION
Attorney Docket No.: 2000.004.00/US

PTO/SB/08A (10-01)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT FORM PTO-1449 (Modified) <i>(USE AS MANY SHEETS AS NECESSARY)</i>				Application Number	
SHEET	1	OF	1	Filing Date	August 11, 2003
				First Named Inventor	Andrew E. Horch
				Art Unit	
				Examiner Name	
Attorney Docket Number	2000.004.00/US				

U.S. PATENT DOCUMENTS					
Examiner Initials	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number- Kind Code (if known)			
V	US-6,462,359	10-08-02	Nemati et al.		
	US-6,229,161	05-08-01	Nemati et al.		

FOREIGN PATENT DOCUMENTS					
Examiner Initials	Cite No.1	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Country Code 3-Number 4 - Kind Code 5 (if known)			
					T6

OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T2
		Country Code 3-Number 4 - Kind Code 5 (if known)			
V	C.K. CHEN; <i>SLOTFET Fabrication of Self-Aligned Sub-100-nm Fully-Depleted SOI CMOS</i> ; 2000 IEEE International SOI Conference, pp. 82-83, Oct. 2000				
V	C.K. CHEN; <i>Fabrication of Self-Aligned 90-nm Fully Depleted SOI CMOS SLOTFETs</i> ; IEEE Electron Device Letters, Vol. 22, No. 7, pp. 345-347, July 2001				

Examiner Signature	Vikhi Trinh	Date Considered	03/23/05
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